

Specification	AXEVA01	Rev.: 1	Date: 2019-06-26
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**Type: Evaluation Board for CO42, CO02, CO41, CO43 and CO08
Package with Sine Wave or HCMOS output**

Parameter	min.	typ.	max.	Unit	Description
Frequency range (Note 1)			200 1300	MHz MHz	Standard Output RF Output
RF output (Note 1)	Sine wave & HCMOS Sine wave				Standard Output RF Output
Nominal output load (Note 2)					
Sine wave HCMOS	50 Ohm 10 pF // 500 Ohm				Max. VSWR 1:2 Voltage ratio 1:10
Supply voltage V_s			15	V	
Transient Protection (Note 3)	Yes				15 V TVS Diode + 1000 μ F
Reverse Polarity Protection (Note 3)	Yes				5 A Schottky Diode
EFC input	All packages				All inputs connected
VREF output	CO41, CO43, CO08				Jumper
Packages (Note 4)					
CO42 (Half-DIL) CO02 (DIL14) CO41 (20x20) CO43 (25x25) CO08 (Eurocase)	4-Pin 4-Pin 5-Pin 5-Pin 5-Pin				Pin \varnothing 0.45 mm Pin \varnothing 0.45 mm Pin \varnothing 0.45 mm Pin \varnothing 0.8 mm Pin \varnothing 0.8 mm
Operable temperature range	-55		+95	$^{\circ}$ C	
Ordering code	AXEVA01_Rev.1				

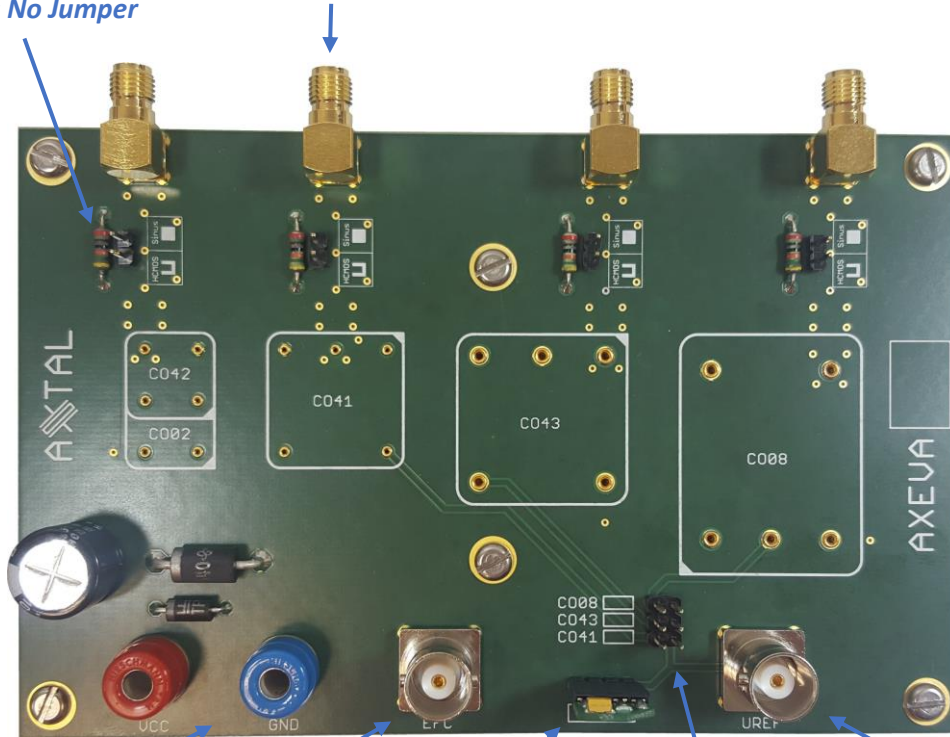
Notes:

1. The evaluation board is assembled with standard output option, which can be easily set to sine wave or HCMOS output via jumper on the top side. For higher frequencies, which require a controlled 50 Ohm impedance, the output can be assembled on the bottom side to be a fixed 50 Ohm transmission line. See description below.
2. The nominal load is present at the oscillator output with 50 Ohm termination at the evaluation board. The HCMOS output uses a 450 Ohm series resistance, which results in 1:10 voltage ratio at the termination.
3. The evaluation board incorporates protection against voltage transients and reverse polarity for the oscillator, but the evaluation board may be permanently damaged after such events. Please make sure to prevent excessive voltage transients and reverse polarity.
4. Package specifications in accordance with IEC60679-3.

Evaluation Board Description

[1] Output Jumper:
Sine wave: Jumper
HCMOS: No Jumper

[2] RF Output:
SMA Connector (F)



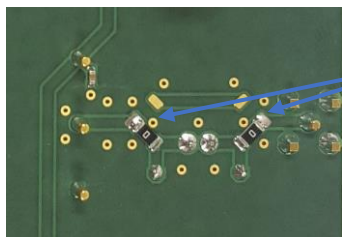
[3] Supply Input:
Banana Plug

[4] EFC Input:
BNC Connector

[5] VREF/EFC Jumper:
Sets EFC to VREF/2
Includes RC low pass
for noise suppression

[6] VREF Jumper:
Selection of package
for VREF output

[7] VREF Output:
BNC Connector



[8] Output Option Jumpers:
SMD 1206 0 Ohm resistors
Standard option (lower branch) – Selection with [1]
RF option (upper branch) – Fixed 50 Ohm transmission line

Revision History

Rev.	Drawing	Date [dd.mm.yyyy]	Remarks	Author	Checked
1	D0	26.06.2019	First issue	HH	DD